



PTO/SB/08b(08-03)

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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/660,986
(Use as many sheets as necessary)		Filing Date	September 12, 2003
Sheet 1 of 1		First Named Inventor	Louis K. Scheffer
		Art Unit	Not yet assigned 2825
		Examiner Name	Not yet assigned YUTHE
		Attorney Docket Number	CA7016672001

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
VS	1	"International Technology Roadmap for Semiconductors", 2001		
1	2	McINERNEY, RORY et al., "Methodology for Repeater Insertion Management in the RTL, Layout, Floorplan and Fullchip Timing Databases of Itanium Microprocessor", International Symposium on Physical Design, 2000, pp. 99-104.		
	3	CHINNERY, D.G. et al., "Closing the Gap between ASIC and Custom: An ASIC Perspective", Proceedings of the Design Automation Conference, June 2000, pp. 337-342.		
	4	CARLONI, LUCA P. et al., "Theory of Latency-Insensitive Design", IEEE Transactions in Computer-Aided Design of Integrated Circuits and Systems, Vol. 20, No. 9, September 2001, pp. 1059-1078.		
	5	CARLONI, LUCA P. et al., "Latency Insensitive Protocols", Computer Aided Verification, 11 th International Conference, CAV'99, Trento, Italy, July 1999, pp123-133.		
	6	"CARLONI, LUCA P. et al., "A Methodology for Correct-by-Construction Latency Insensitive Design", Proc. ICCAD, San, Jose, CA, November 1999, pp. 309-315.		
VS	7	LU, RUIBING et al., "Flip-Flop and Repeater Insertion for Early Interconnect Planning", Proceedings of the 2002 Design, Automation and Test in Europe Conference, 2002.		

Examiner Signature	VUTHE SIEK	Date Considered	4/6/05
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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1 of 1

Complete if Known

Application Number	10/660,986
Filing Date	September 12, 2003
First Named Inventor	Louis K. SCHEFFER
Art Unit	2825
Examiner Name	Vuthe Siek
Attorney Docket Number	CA7016672001

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YY	Name of Patentee or Application of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number – Kind Code ² (if known)			
BS	1	US-2002/0184600 A1	12-05-02	Sachs	
	2	US-2003/0145264 A1	07-31-03	Siegel et al.	
	3	US-2003/0182634 A1	09-25-03	Chang et al.	
	4	US-2003/0192020 A1	10-09-03	Collins	
	5	US-5,175,843	12-29-92	Casavant et al.	
	6	US-5,911,063	06-08-99	Allen et al.	
	7	US-6,292,926 B1	09-18-01	Fukui et al.	
	8	US-6,415,430 B1	07-02-02	Ashar et al.	
	9	US-6,434,731 B1	08-13-02	Brennan et al.	
	10	US-6,453,454 B1	09-17-02	Lee et al.	
BT	11	US-6,609,228 B1	08-19-03	Bergeron et al.	

FOREIGN PATENT DOCUMENTS

**Examiner's
Signature**

WUTHER SIER

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		Art Unit		2825	
		Examiner Name		Vuthe Siek	
Attorney Docket Number	CA7016672001				

NON PATENT LITERATURE DOCUMENTS

Examiner's Signature	WUTHE SIEK	Date Considered	4/6/05	
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